



Anantha Lakshmi

INSTITUTE OF TECHNOLOGY & SCIENCES

(AUTONOMOUS)

(Approved by A.I.C.T.E, New Delhi, Accredited by NBA & NAAC, and Affiliated to J.N.T. University Anantapur)
Near SK University, Itikalapalli-515 721 (V), Ananthapuramu (Dist.)

ALTS/ES/Notification-06/MAY -2025

Date: 10-05-2025

EXAMINATION BRANCH

NOTIFICATION

M.TECH (ALTS R24) Regular Examinations – JUNE 2025

I M.Tech II Semester Regular Examinations JUNE 2025

Candidates appearing for the above examinations commencing from **02-06-2025** are informed that the applications will be received as per the time schedule given below:

EXAM REGISTRATION	START DATE	LAST DATE
Without Late Fee	12-05-2025	16-05-2025
With Late Fee of Rs. 100/-	17-05-2025	19-05-2025
With Late Fee of Rs. 1000/-	20-05-2025	22-05-2025
With Late Fee of Rs. 3000/-	23-05-2025	24-05-2025
With Late Fee of Rs. 5000/-	26-05-2025	28-05-2025

EXAMINATION FEE		
1	For the whole examination (Regular)	1850/-

Note:

1. Payment of examination fee does not guarantee eligibility for appearing examinations.
2. The eligibility of the student depends on the fulfilment of the academic requirements of the institute.
3. No student is allowed for laboratory examinations without Hall Ticket.
4. Examination fee is non-refundable.
5. Detailed time tables will be notified in the website www.alits.ac.in.


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Date: 10/05/2025

EXAMINATION BRANCH

I M.TECH II Semester (R24) Regular Examinations June 2025

Time: 10:00 AM to 01:00PM

Date	STRUCTURAL ENGINEERING	ELECTRICAL POWER SYSTEMS	VLSI	COMPUTER SCIENCE AND ENGINEERING
02-06-2025	Structural Dynamics 24ALMTSE201T	Power System Stability and Control 24ALMTSS201T	CMOS Mixed Signal IC Design 24ALMTVL201T	Advanced Operating Systems 24ALMTCSS201T
04-06-2025	Finite Element Methods for Structural Engineering 24ALMTSE202T	FACTS Controllers 24ALMTSS202T	Physical Design Automation 24ALMTVL202T	Internet of Things 24ALMTCSS202T
06-06-2025	Experimental Stress Analysis 24ALMTSE203Tb	Reactive Power Compensation & Management 24ALMTSS203Tc	Semiconductor Memory Design and Testing 24ALMTVL203Tb	Service Oriented Architecture 24ALMTCSS203Tb
10-06-2025	Advanced Reinforced Concrete Design 24ALMTSE204Tc	Power Quality 24ALMTSS204Ta	Low Power VLSI Design 24ALMTVL204Ta	Distributed Systems 24ALMTCSS204Tb

- Note: (i) Any omissions or clashes in this time table may please be informed to the undersigned immediately.
(ii) If any discrepancies are found, the same may be informed to the undersigned immediately.

V.V.V.
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